1. (currently amended) A switch matrix, comprising: at least one row conductor;

at least one column conductor, wherein each of said at least one row conductor and said at least one column conductor are capable of being driven with a predetermined voltage level, and being capable of being read therefrom a voltage level; only one diode voltage drop switchably connected between each said at least one row conductor and each said at least one column conductor; and

a plurality of switching elements adapted to connect said at least one row conductor to said at least one column conductor, said plurality exceeding a number obtained by multiplying together a number of said at least one row conductor and a number of said at least one column conductor;

wherein some of said plurality of switching elements are implemented to allow current to flow bi-directionally therethrough, and others of said plurality of switching elements are restricted to allow current to flow only in one direction therethrough.

2. (canceled)

3. (canceled)

4. (previously presented) The switch matrix according to claim 1, wherein:

at least one of said plurality of switching elements is a temporary connection type switching element.

5. (previously presented) The switch matrix according to claim 1,

wherein:

at least one of said plurality of switching elements is a momentary

switch.

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6. (previously presented) The switch matrix according to claim 1, wherein:

at least one of said plurality of switching elements is a persistent connection type switching element.

7. (canceled)

8. (previously presented)/The switch matrix according to claim 1,

wherein:

said plurality is twice a number obtained by multiplying together said number of said at least one row conductor and said number of said at least one column conductor.

9. (currently amended) A switch matrix, comprising: a plurality of row conductors;

a plurality of column conductors, each of said plurality of row conductors and each of said plurality of column conductors are capable of being driven with a predetermined voltage level, and being capable of being read therefrom a voltage level;

only one diode voltage drop switchably connected between each said at least one row conductor and each said at least one column conductor; and

a plurality of switching elements including at least one momentary push button adapted to connect at least one of said plurality of row conductors to at least one of said plurality of column conductors;

wherein a total number of switching elements of said plurality of switching elements exceeds a product of a total number of row conductors of said plurality of row conductors and a total number of column conductors of said plurality of column conductors; and

wherein some of said plurality of switching elements are implemented to allow current to flow bi-directionally therethrough, and others of said plurality of switching elements are restricted to allow current to flow only in one direction therethrough.

10. (original) The switch matrix according to claim 9, wherein: said total number of switching elements is twice said product of said total number of row conductors and said total number of column conductors.

11. **(**canceled)

12/. (canceled)

13. (canceled)



14. (currently amended) A switch matrix, comprising:

a plurality of row conductors;

a plurality of column conductors;

enly one diode voltage drop switchably connected between each said at least one row conductor;

a plurality of momentary switching elements adapted to momentarily connect at least one of said plurality of row conductors to at least one of said plurality of column conductors, and

a plurality of persistent switching elements adapted to persistently connect at least one of said plurality of row conductors to at least one of said plurality of column conductors;

wherein a total of said plurality of momentary switching elements and said plurality of persistent switching elements exceed a number obtained by multiplying together a number of said plurality of row conductors and a number of said plurality of column conductors; and

wherein some of said plurality of momentary switching elements are implemented to allow current to flow bi-directionally therethrough, and others of said plurality of momentary switching elements are restricted to allow current to flow only in one direction therethrough.

15. (previously presented) The switch matrix according to claim

said at least one momentary switching element includes at least one push.



14, wherein

16. (currently amended) A method of scanning a switch matrix, comprising:

permitting some of a plurality of switches to allow current to flow bidirectionally therethrough, and others of said plurality of switches to be restricted to allow current to flow only in one direction therethrough;

persistently connecting at least one of a plurality of row conductors to at least one of a plurality of column conductors,

driving one at a time each of said plurality of row conductors with a predetermined row voltage level;

monitoring each of said plurality of column conductors for a given voltage drop including a single diode voltage drop corresponding to a closed switch one of said plurality of switches to a driven one of said plurality of row conductors, while one of said plurality of row conductors is being driven with said predetermined row voltage level;

driving one at a time each of said plurality of column conductors with a predetermined column voltage level; and

monitoring each of said plurality of row conductors for a given voltage drop including a single diode voltage drop corresponding to a closed switch one of said plurality of switches to a driven one of said column conductors, while one of said plurality of column conductors is being driven with said predetermined column voltage level;

wherein a number of <u>said plurality of</u> switches is increased by said persistent connection exceeding a total number obtained by multiplying together a number of said plurality of row conductors and a number of said plurality of column conductors.



17. (currently amended) The method of scanning a switch matrix in accordance with claim 16, further comprising:

detecting a closure of a first one of a <u>said</u> plurality of switching elements <u>switches</u> based on a presence of said predetermined column voltage level during monitoring of said plurality of row conductors; and

detecting a closure of a second one of a <u>said</u> plurality of switching elements different from said first one of said plurality of switching elements <u>switches</u>, said detection of said closure of said second one of <u>said</u> plurality of <u>switching element</u> <u>switches</u> being based on a presence of said predetermined row voltage level during monitoring of said plurality of column conductors.

18. (currently amended) The method of scanning a switch matrix in accordance with claim 17, wherein:

said plurality of switching elements switches includes a momentary push button.

19. (currently amended) The method of scanning a switch matrix in accordance with claim 17, wherein:

said plurality of switching elements switches includes a persistent switch.

20. (currently amended) The method of scanning a switch matrix in accordance with claim 17, wherein:

said plurality of switching elements switches includes a momentary push button and a persistent switch.

21. (new) The switch matrix according to claim 1, wherein: said one direction allows current to flow from one of said at least one row, to one of said at least one column.

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22. (new) The switch matrix according to claim 1/wherein: only one diode voltage drop is switchably connected between each said at least one row conductor and each said at least one column conductor.

23. (new) The switch matrix according to claim 9, wherein: said one direction allows current to flow from one of said at least one row, to one of said at least one column.

24. (new) The switch matrix according to claim 9, wherein: only one diode voltage drop is switchably connected between each said at least one row conductor and each said at least one column conductor.